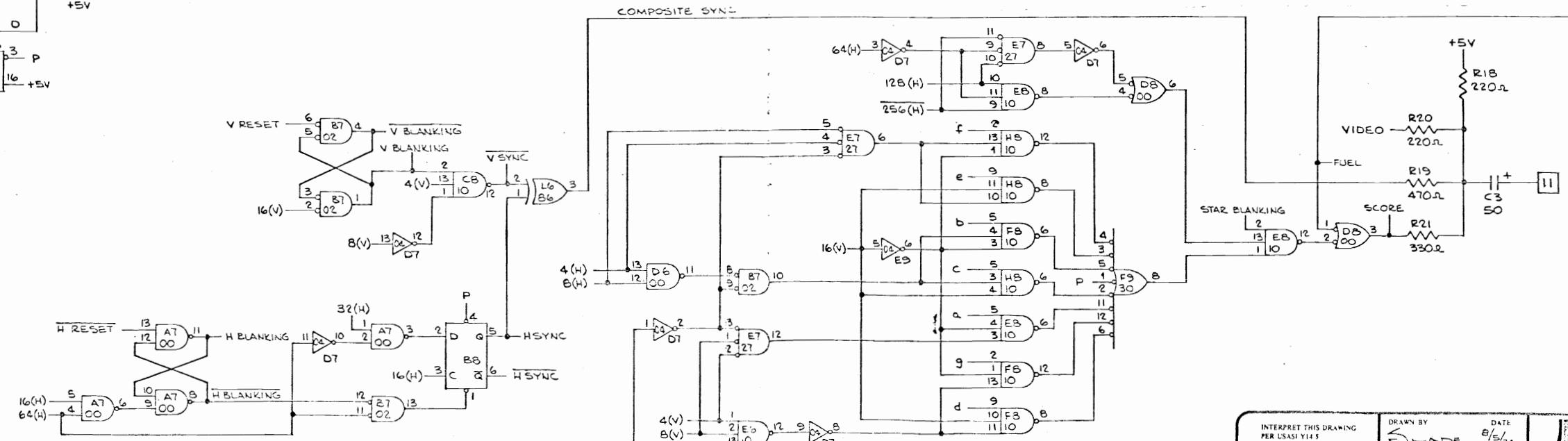


NOTES: UNLESS OTHERWISE SPECIFIED

1. ALL JK FLIP-FLOPS ARE 74107
2. ALL D TYPE FLIP-FLOPS ARE 7474
3. ALL DIODES ARE IN514
4. ALL RESISTORS ARE 1/4W CARBON 10%
5. ALL PNP TRANSISTORS ARE 2N3644 OR EQUIVALENT.
6. ALL NPN TRANSISTORS ARE 2N3643 OR EQUIVALENT.



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INTERPRET THIS DRAWING
PER USASI Y1.5

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES ON

FRACTIONS + 1/16 ± .125
ANGLES + 10° ± 4° 02'
SURFACE FINISH ✓ XXX ± .010

MATERIAL

APPLICATION

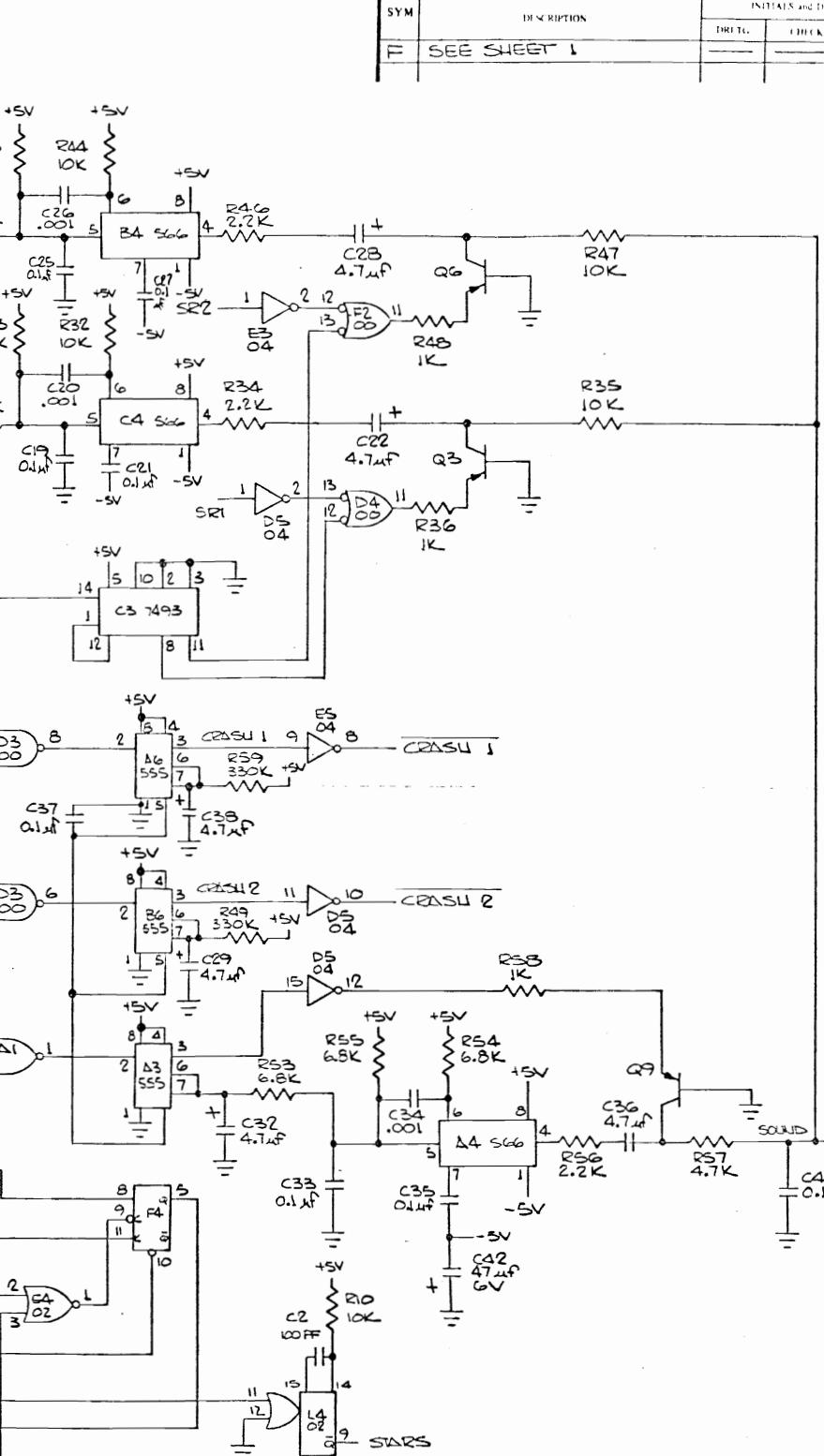
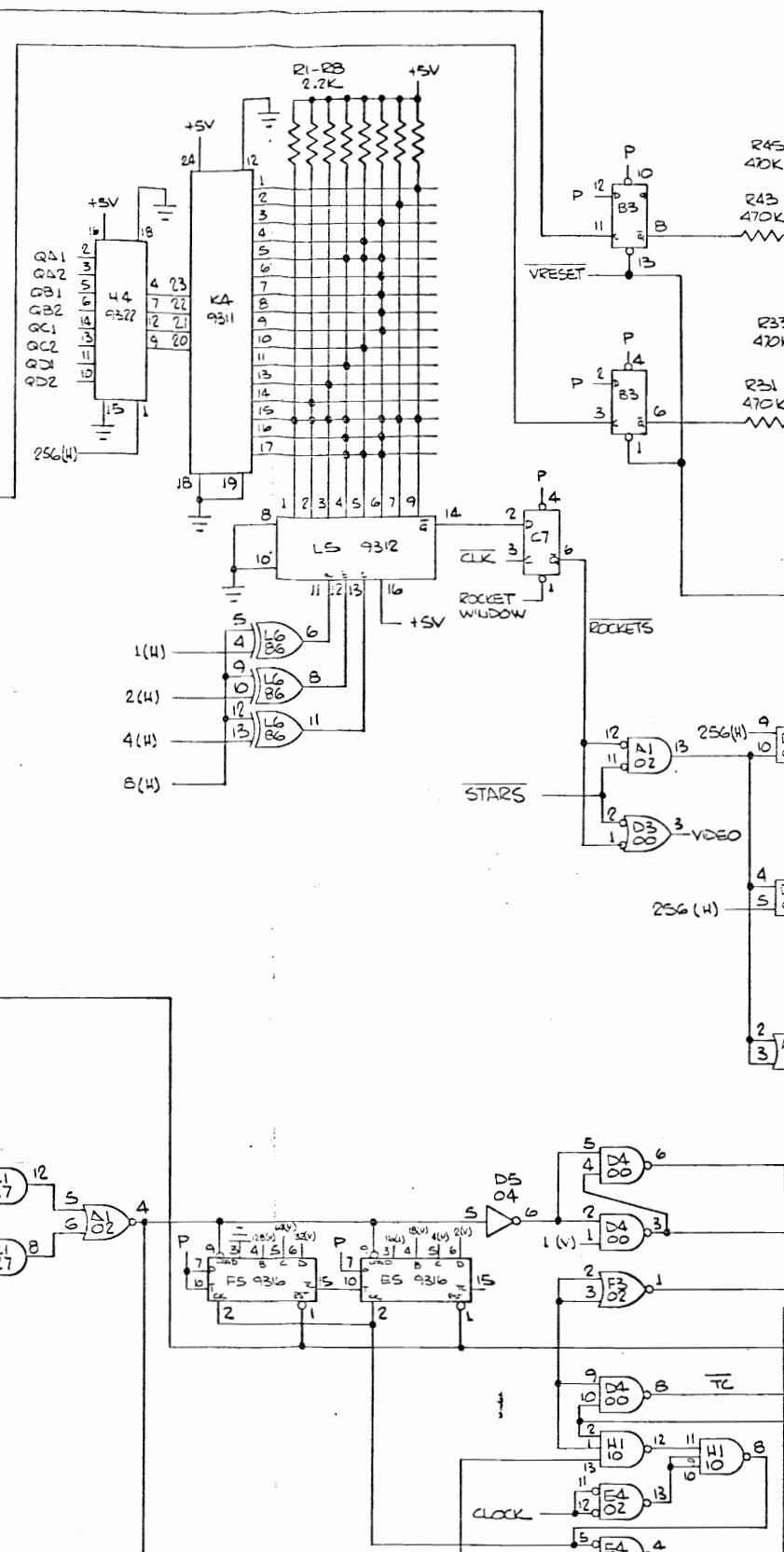
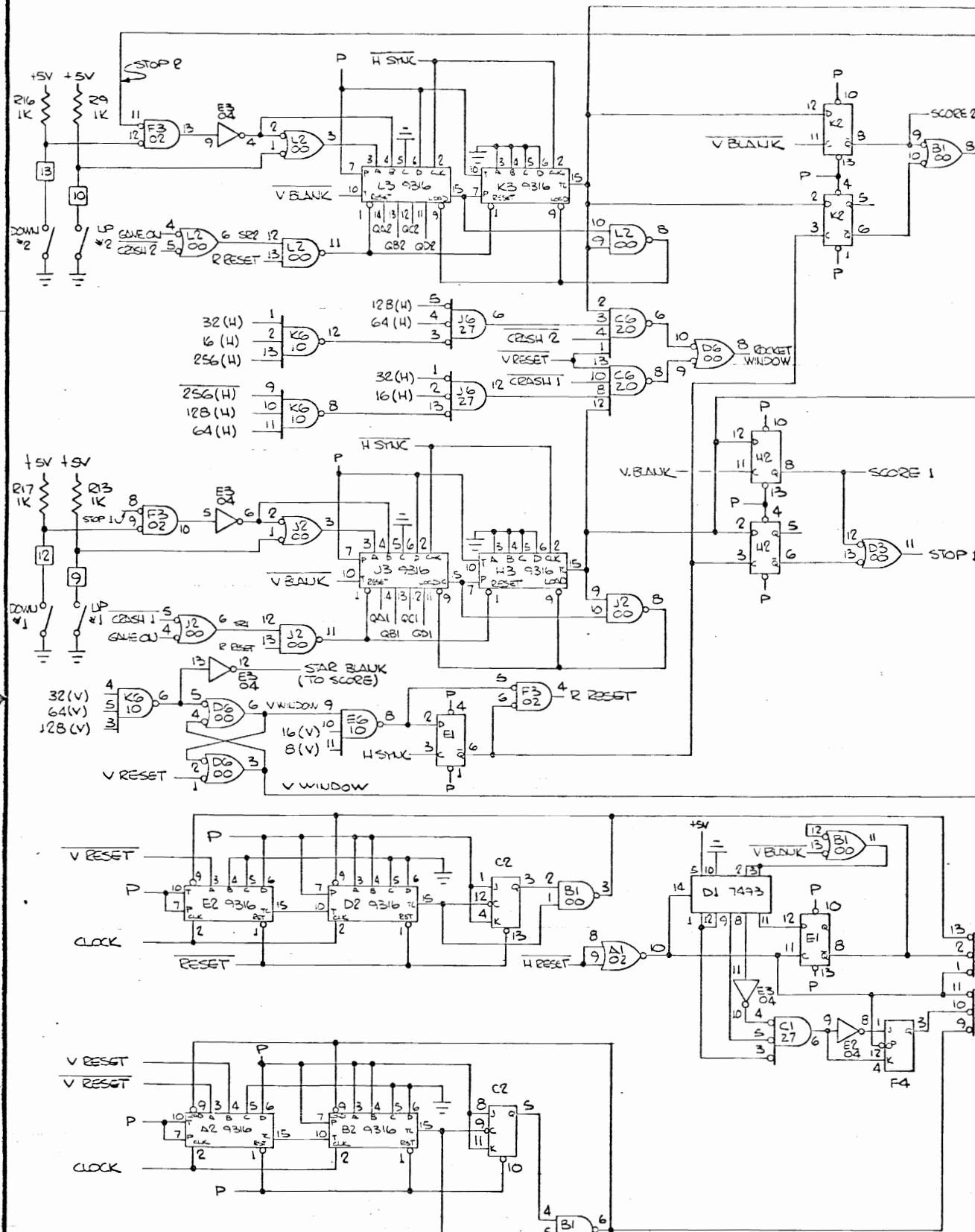
DRAWN BY
S. LADEN
DATE
8/5/74
CHECKED
8/6/74
DESIGN ENGINEER
PROJECT ENGINEER
DOCUMENT CONTROL
APPROVED
M.J.

ATARI INCORPORATED
14600 Winchester Boulevard
Los Gatos, California 95030

TITLE
SCHEMATIC
SPACE RACE

SIZE
D DRAWING NO
000780

F

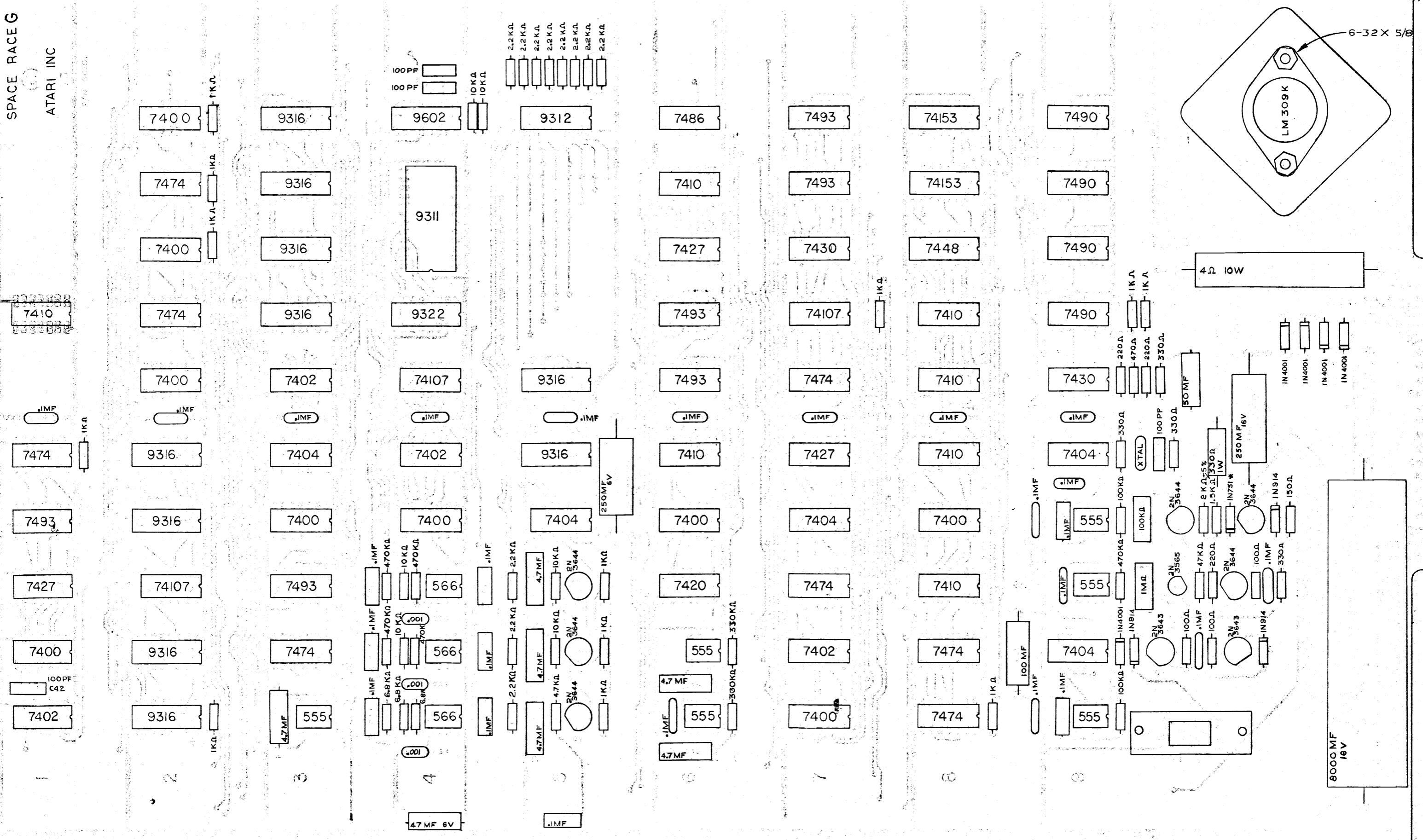


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<p>INTERPRET THIS DRAWING PER USASI Y14.5</p> <p>UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON:</p> <p>FRACTIONS + 1/16 - .00 -.03 ANGLES + 1° - .00 -.03 SURFACE FINISH ✓ .000 -.010</p> <p>MATERIAL:</p>	DRAWN BY <u>DC</u>	DATE 8-23-74
	CHECKED	
	DESIGN ENGINEER	
	PROJECT ENGINEER	
	DOCUMENT CONTROL <u>MC</u>	
	APPROVED	
 ATARI TITLE SCHEMATIC DIAGRAM SPACE RACE		
SIZE DRAWING NO. D 000780		REV F
SCALE		SHEET 2 OF 2

SPACE RACE G

ATARI INC



* USE CR5 OR R28, R29, + Q2

STUFFING for SR
000803

4

3

2

1

D

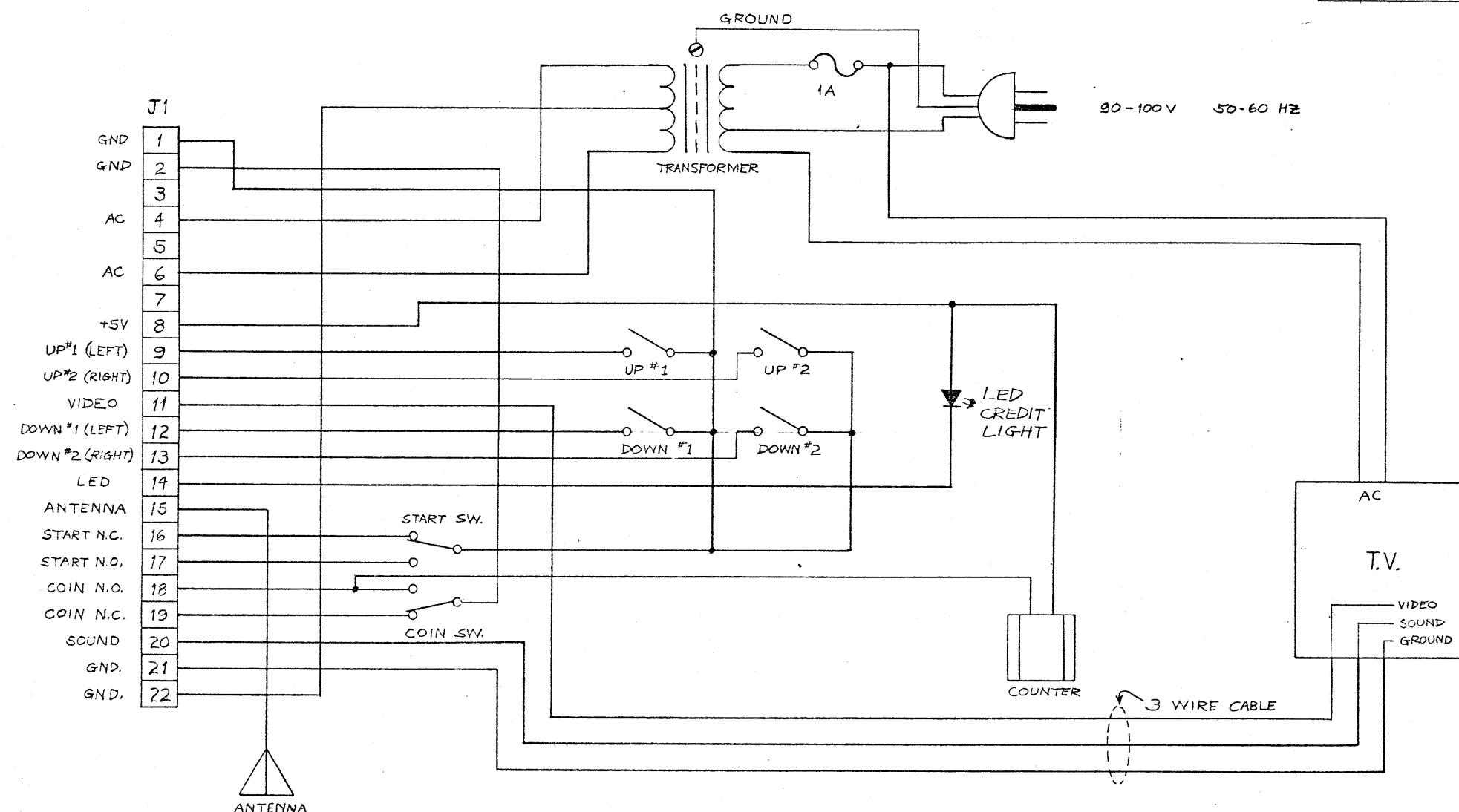
D

C

B

REV

A

NOTES:

INTERPRET THIS DRAWING PER USASI Y14.5		DRAWN BY T. Fowlks 5/16/74	DATE 5/16/74	ATARI INCORPORATED 14600 Winchester Boulevard Los Gatos, California 95030
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON: FRACTIONS = $\pm\frac{1}{16}$.x = $\pm.1$ ANGLES = $\pm 1^\circ$.xx = $\pm .03$ SURFACE FINISH ✓ .xxx = $\pm .010$		CHECKED	DESIGN ENGINEER	
PROJECT ENGINEER <i>N. Lang</i>		DOCUMENT CONTROL <i>M.J.</i>	TITLE WIRING HARNESS S.R.	
NEXT ASSY SPACE RACE		APPROVED <i>R. L. Miller</i> 6/25/74	SIZE C	DRAWING NO. 000771
USED ON APPLICATION		SCALE NONE	REV B	SHEET 1 OF 1